

A METHOD AND A CIRCUIT SYSTEM FOR USING EQUIVALENT INTEGRATED-CIRCUIT DEVICES OPERATING AT DIFFERENT VOLTAGES

TECHNICAL FIELD

5 The present invention relates to the manufacture of circuit systems containing integrated-circuit devices and, more particularly, to a circuit and method of using, in a circuit system which is designed to operate with a first integrated-circuit device operating at a first supply voltage, a second integrated-circuit device having the same functional characteristics as the first device and comprising at least a portion operating at a
10 second supply voltage.

BACKGROUND OF THE INVENTION

It is known that, in the manufacture of integrated circuits, there is a tendency to reduce as far as possible the area of the substrate of semiconductor material, 15 typically silicon, in which the integrated circuit is formed. The reduction in size is achieved by the use of more and more refined techniques and tooling which increase the precision of the photolithographic processes and improve control over diffusion.

In the manufacture of MOS digital integrated circuits, the development of the technology is evident basically as a reduction in the minimum channel length which can 20 be achieved for the transistors both of the logic circuits and of the memory cells, and as a reduction in the depth of the diffused source and drain regions of the transistors. These dimensional reductions are advantageous not only because of the saving in area involved, but also because of some functional aspects such as lower energy consumption and faster speed of operation.

25 However, there are cases in which it is not possible to make use of these advantages. An example is a digital integrated circuit designed to perform a certain function as a component of a complex circuit system which is mounted, together with other

components, on a printed circuit board. The supply voltage of the integrated circuit is set at the design stage at an optimal value which is determined by the minimum length of the channels of the transistors of the logic circuits. If the integrated circuit as a whole, or a portion thereof, is reproduced on a reduced scale with the use of more advanced 5 manufacturing techniques, the optimal supply voltage is also reduced. For example, upon changing from 0.35 μ m technology, which requires a supply voltage of 3.3 V \pm 10%, to 0.25 μ m technology, the supply voltage must be reduced to 2.5 V \pm 10%. In order to use a new-generation integrated circuit in the circuit system which was designed for using the integrated circuit of the preceding generation, it is necessary to modify the printed circuit 10 board, for example, by adding a voltage reducer thereto. This modification is necessary when the average life of the product which contains the circuit system is longer than that of the technology by which the integrated circuits used therein are produced. (Currently, the life of a technological generation is slightly less than one year.) It is therefore sometimes necessary to forego the advantages of technological progress and to keep obsolete 15 production lines in operation to provide the supplies and the spare parts which are required by the manufacturers of the products, or to modify the circuit systems contained in these products in order to adapt them to equivalent new-generation integrated circuits. The first solution is disadvantageous for the manufacturer of the integrated-circuit devices and the second is disadvantageous for the manufacturer of the circuit systems; naturally, both of 20 these solutions adversely affect the cost of the product.

SUMMARY OF THE INVENTION

The object of the present invention is to define a method and to provide a circuit system to enable integrated-circuit devices of different generations to be used in the 25 same circuit system without modifications or adaptations.

According to one embodiment of the invention, a circuit system is provided, configured to accommodate an integrated circuit. First and second voltage supply terminals are provided, which supply a voltage level consistent with the voltage requirements of a

first-generation integrated circuit. An additional terminal is provided, with a filter capacitor connected between the additional terminal and the second voltage supply terminal. In the event that a first generation integrated circuit is used, the first and second voltage supply terminals provide power for the integrated circuit, while a direct connection is made within 5 the integrated circuit between the terminal pins that make contact with the first voltage supply terminal and the additional terminal.

In the event that a subsequent-generation integrated circuit is used, a voltage reducer circuit is integrated into the subsequent-generation integrated circuit, with an input of the voltage reducer circuit connected to the first voltage supply terminal, and an output 10 of the voltage reducer circuit providing a reduced voltage to the integrated circuit, according to the requirements of the integrated circuit. The output of the voltage reducer circuit is also connected to the additional terminal, where the filter capacitor provides filtering for the reduced voltage.

BRIEF DESCRIPTION OF THE DRAWINGS

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The invention will be understood better from the following detailed description of an embodiment thereof, given with reference to the appended drawings, in which:

Figures 1 and 2 show schematically two circuit systems of the prior art 20 comprising integrated circuits of two different generations, and

Figures 3 and 4 show schematically two circuit systems according to the invention comprising integrated circuits of two different generations.

DETAILED DESCRIPTION OF THE INVENTION

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Figure 1 shows schematically a printed circuit board, indicated PCB1, which contains an integrated-circuit device IC1g (the suffix 1g indicates that the integrated circuit device is of the “first generation”) and a voltage supply V. The device IC1g has a plurality

of terminal pins connected to corresponding connection areas, or contact pads, of the board PCB1. The drawing shows only four pins connected to corresponding areas a1 - a4 of the board PCB1. Two of these pins, indicated VDD1 and VSS, are connected to areas a1, a2 which are connected to the voltage supply V. The active portion of the integrated circuit is 5 represented by a block A with terminals a11, a21 connected to the pins VDD1 and VSS. The board PCB1 contains various other components, not shown, which, together with the device IC1g, constitute the circuit system of an electronic device, for example, a cellular telephone. The voltage of the supply V is set at the design stage so as to be suitable for the characteristics of the device IC1g. In this embodiment, the supply V is shown as a 10 component mounted on the board PCB1 but, naturally, it could also be external to the board.

Figure 2 shows how the circuit of the printed circuit board, indicated PCB2 in this case, has to be modified in order to use a second-generation integrated-circuit device IC2g produced by a reduction in scale of the dimensions of the first-generation device 15 IC1g. The device IC2g is functionally equivalent to the device IC1g and has the same pin arrangement as the device IC1g, but its active portion A2 requires a lower supply voltage. For this purpose, the supply voltage V is reduced and stabilized by a regulator RG which has its output terminals connected to the supply terminals VDD2 and VSS of the device IC2g, and by a filter capacitor C connected in parallel with the output of the regulator.

20 According to the invention, as shown in Figure 3, in which parts identical to those of Figure 1 are identified by the same reference symbols, both the printed circuit board PCB and the first-generation integrated circuit IC3g are designed taking account of possible future reductions in scale of the integrated circuit. More particularly, the device IC3g has an additional pin, indicated VDD2g, and an internal connection SC which short- 25 circuits the pins VDD1g and VDD2g. The board PCB in turn has an additional connection area a+ to which the additional pin VDD2g is fixed and a capacitor C connected between the additional area a+ and the area a2 which is connected to the supply pin VSS. Naturally, the additional pin VDD2g is entirely superfluous from the point of view of the operation of this circuit system and the capacitor C has the sole function of stabilizing the supply

voltage. The capacitor C may be omitted if the stabilization function is not necessary and if there is no provision for integrated-circuit devices of generations subsequent to that of the device IC3g to be mounted on the board PCB.

Only four pins, a1-a4 are shown in Figure 3 for simplicity sake. Of course, 5 other pins are present on the board and chip and, as appropriate, the different voltages are provided to these additional pins.

Figure 4 shows the same board PCB as Figure 3, on which an integrated-circuit device IC4g of a generation subsequent to that of the device IC3g of Figure 3 and having an active portion A2 is mounted. In this case, the integrated-circuit device again 10 has an additional pin VDD2g and has the same pin arrangement as the device IC3g, but contains a voltage reducer/regulator RG instead of the short-circuit SC. The supply voltage V is reduced and stabilized at the value required for the supply of the active portion A2 of the device IC4e. In this case, the capacitor C, which may have a value of between 1 and 10 15 μ F, performs the function of filtering interference present at the supply terminals, for example, such as interference due to switching of the transistors of the logic gates in the circuit block A2.

It should be noted that the higher supply voltage available at the terminal VDD1g can advantageously be used to supply any portions or pins of the integrated circuit which have not been reduced in scale, for example, because they are intended for digital 20 input-output interface functions.

According to the invention, the printed circuit board, PCB does not have to be modified when the integrated circuit mounted thereon is replaced by an equivalent device of a subsequent technological generation, since the modifications necessary for the adaptation are made within the integrated circuit itself by the addition of a voltage regulator 25 circuit. It should be noted that the area required by the regulator is generally very small and, in any case, smaller than the area saved by the reduction in scale. For example, by changing from 0.35 μ m technology to 0.25 μ m technology, an integrated circuit which originally occupied an area of 100 mm^2 can be produced in an area of 50 mm^2 , whereas the area required for a regulator is 2 - 3 mm^2 .

In the event that a printed circuit board is later designed and produced to provide the lower voltage levels of the newer integrated circuit device, the lower supply voltage can be presented on the a+ terminal of chip IC4g, which will bypass the voltage regulator circuit and provide direct supply to the integrated circuit device. The same chip 5 can continue to be used, on the new printer circuit board while the cost savings of not having to design a new printed circuit board with each new chip voltage has been realized.

From the foregoing it will be appreciated that, although specific embodiments of the invention have been described herein for purposes of illustration, various modifications may be made without deviating from the spirit and scope of the invention. Accordingly, the invention is not limited except as by the appended claims.